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(57) Abstract :

A visual debugger device (10) for hardware-software co-simulation environments is disclosed, featuring real-time graphical rendering of system states. The device interfaces with simulation systems (12), includes an input interface (16) and input device (18) for dynamic control, and renders synchronized hardware-software traces on a display (14). It enables timeline-based navigation, parameter tuning, and error diagnosis, significantly reducing debugging complexity and enhancing verification productivity.

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